

Digital Backpropagation with Deep-Learned Chromatic Dispersion Filters

Christian Häger^(1,2)

Joint work with: Henry D. Pfister⁽²⁾, Christoffer Fougstedt⁽³⁾,
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Machine Learning and Fiber-Optic Communications

- What can machine learning contribute to the design of fiber-optic communication systems?

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 - **Equalization** [Shen and Lau, 2011], [Jarajreh et al., 2015], [Giacoumidis et al., 2015], [Zibar et al., 2016], ... ,
 - **Performance monitoring** [Xiaoxia et al., 2009], [Khan et al., 2012], [Tanimura et al., 2016], ...
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1. Is not about black-box neural networks ... but we uncover and exploit an interesting connection between neural networks and the split-step method
2. We use deep learning to jointly optimize, prune, and quantize all linear substeps \Rightarrow ASIC power consumption becomes comparable to linear equalization, even with multiple steps per span

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1. Is not about black-box neural networks ... but we uncover and exploit an interesting connection between neural networks and the split-step method
2. We use deep learning to jointly optimize, prune, and quantize all linear substeps \implies ASIC power consumption becomes comparable to linear equalization, even with multiple steps per span
3. No step-reducing approaches: the spirit behind “deep learning” and “reducing steps” are fundamentally opposed

Outline

1. Introduction
2. Connection between Deep Learning and Digital Backpropagation
3. ASIC Implementation Aspects
4. Wideband Digital Backpropagation via Subband Processing
5. Conclusions

Outline

1. Introduction

2. Connection between Deep Learning and Digital Backpropagation

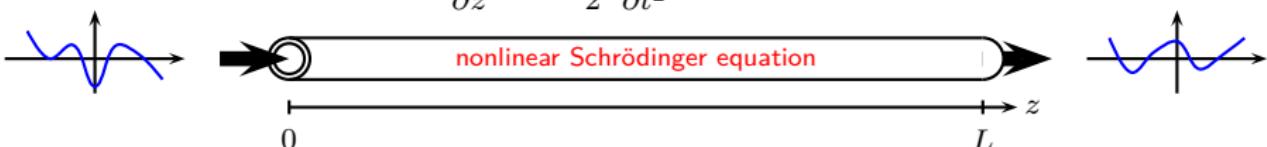
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Digital Backpropagation

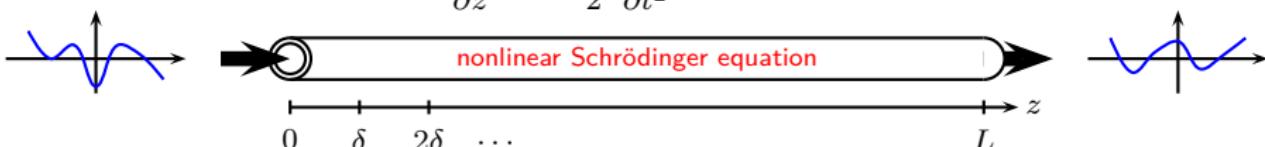
$$\frac{\partial u}{\partial z} = -j \frac{\beta_2}{2} \frac{\partial^2 u}{\partial t^2} + j\gamma u|u|^2$$



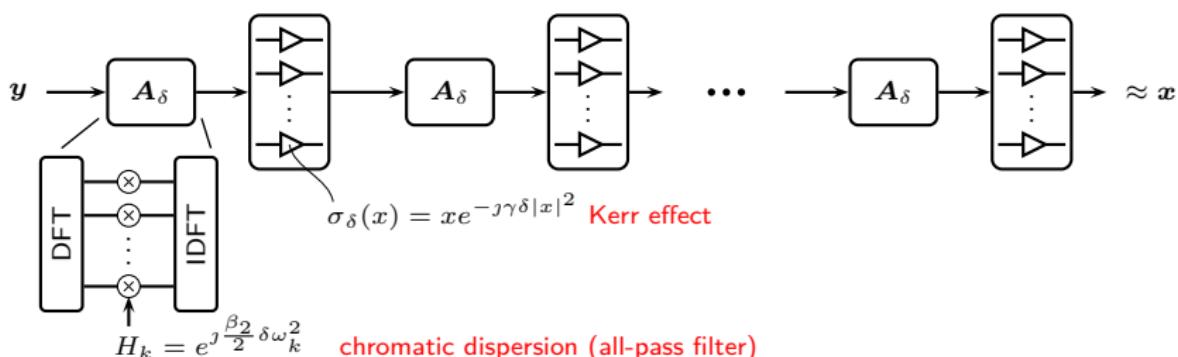
- Invert a partial differential equation **in real time** ([Paré et al., 1996], [Essiambre and Winzer, 2005], [Roberts et al., 2006], [Li et al., 2008], [Ip and Kahn, 2008])

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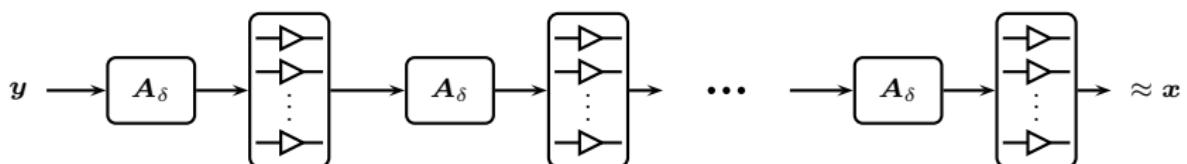
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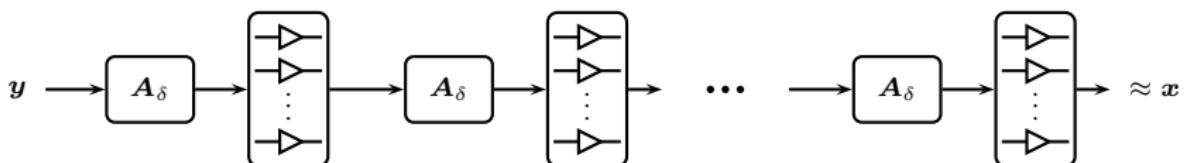
- Invert a partial differential equation **in real time** ([Paré et al., 1996], [Essiambre and Winzer, 2005], [Roberts et al., 2006], [Li et al., 2008], [Ip and Kahn, 2008])
- Split-step Fourier method** with M steps ($\delta = L/M$):



Real-Time Digital Backpropagation

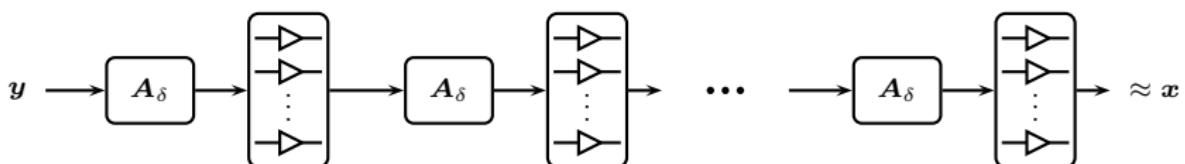


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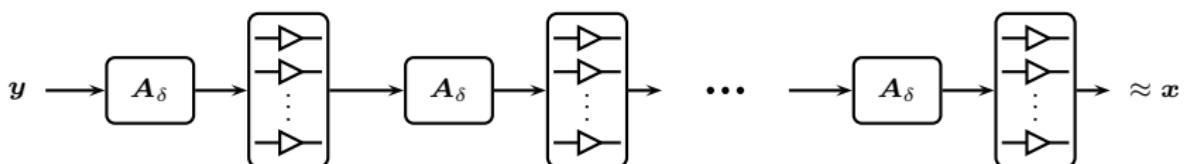
- Widely considered to be impractical (**too complex**): linear equalization is already one of the **most power-hungry DSP blocks** in coherent receivers

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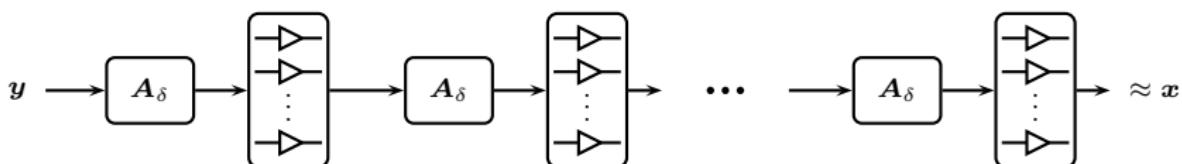
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- Complexity increases with the number of steps $M \Rightarrow$ **reduce M as much as possible** (see, e.g., [Du and Lowery, 2010], [Rafique et al., 2011], [Li et al., 2011], [Yan et al., 2011], [Napoli et al., 2014], [Secondini et al., 2016], ...)

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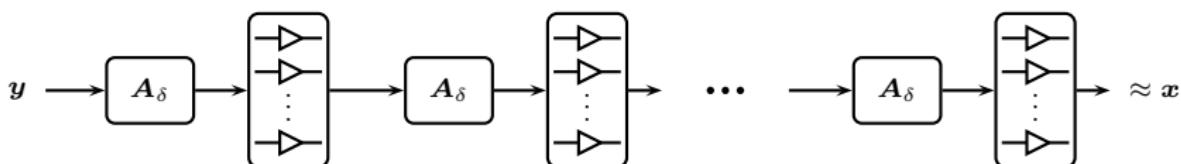
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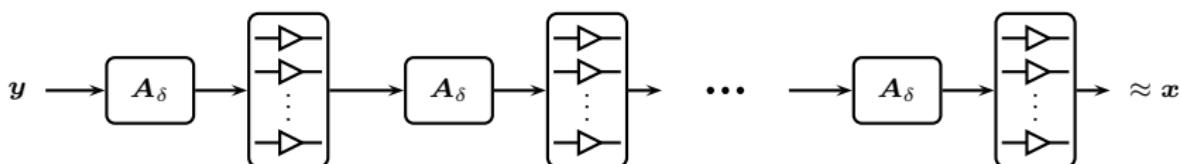
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Main contribution

Joint optimization and sparsification of all linear substeps leads to **efficient digital backpropagation**, even with many steps.

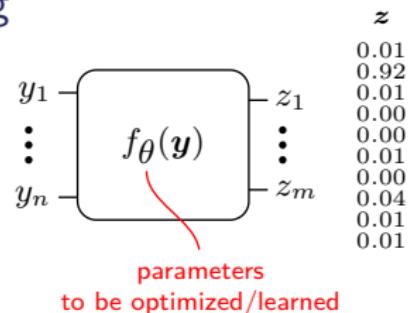
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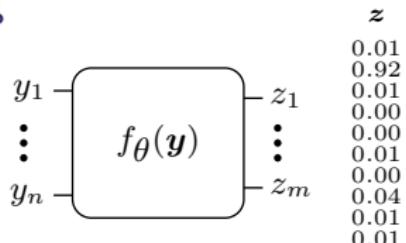
handwritten digit recognition (MNIST: 70,000 images)

28 × 28 pixels $\implies n = 784$

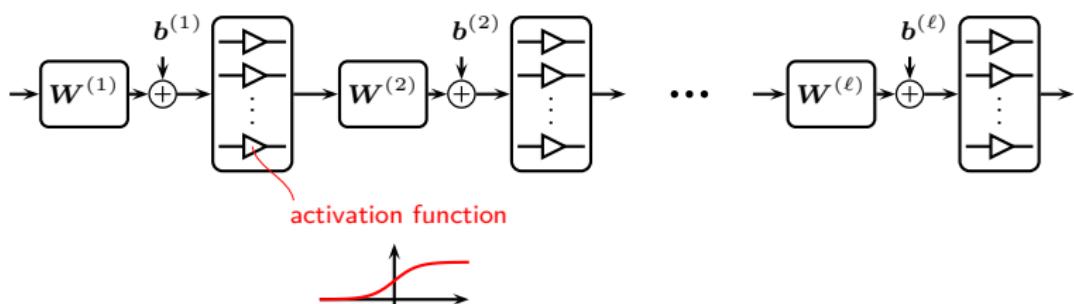


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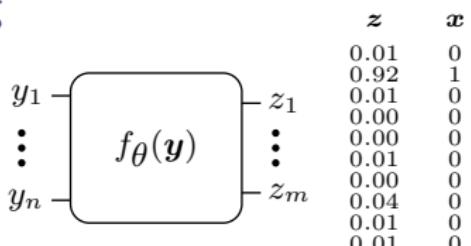


How to choose $f_\theta(y)$? Deep feed-forward neural networks

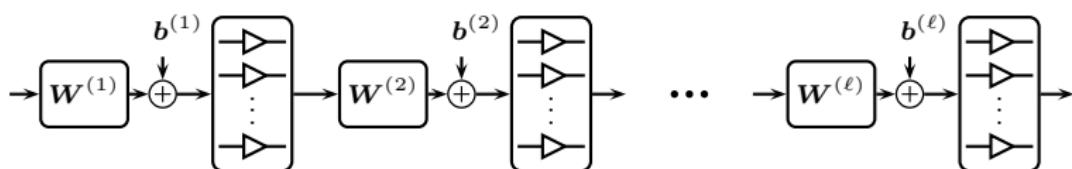


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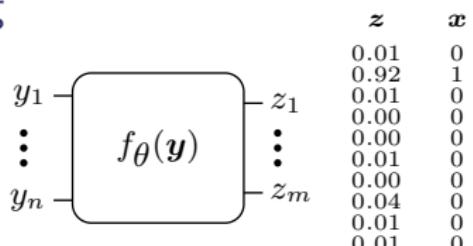


How to optimize $\theta = \{W^{(1)}, \dots, W^{(\ell)}, b^{(1)}, \dots, b^{(\ell)}\}$? Deep learning

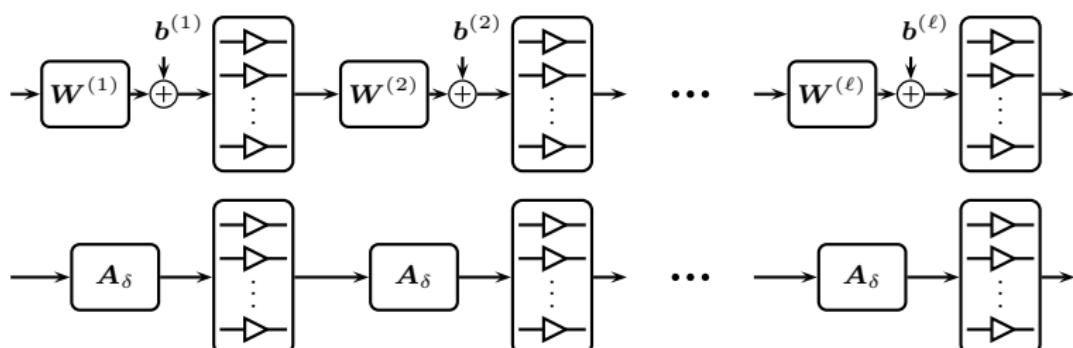
$$\min_{\theta} \sum_{i=1}^N \text{Loss}(f_{\theta}(y^{(i)}), x^{(i)}) \triangleq g(\theta) \quad \text{using} \quad \theta_{k+1} = \theta_k - \lambda \nabla_{\theta} g(\theta_k) \quad (1)$$

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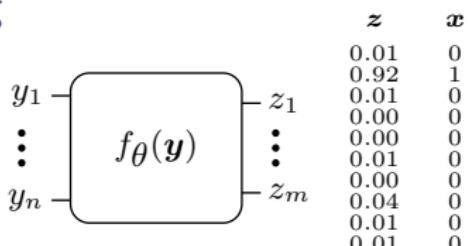


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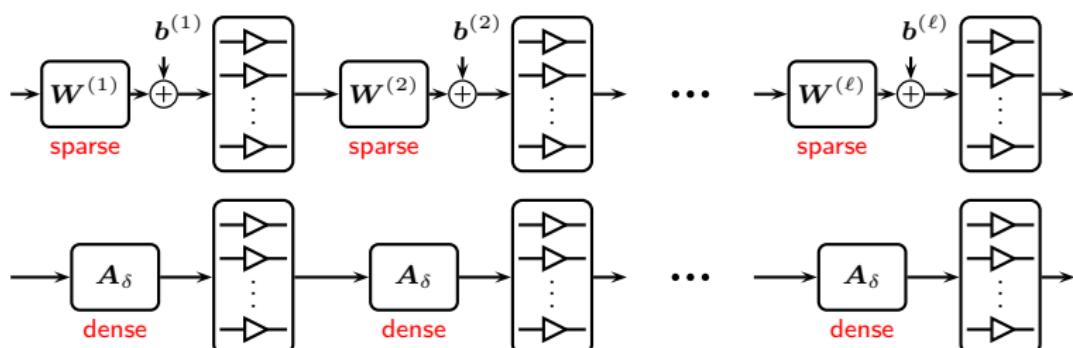


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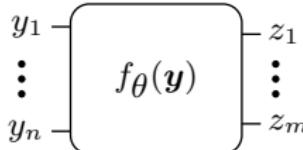
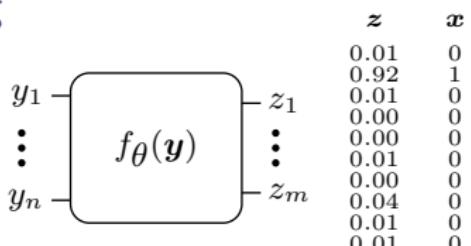


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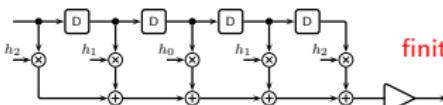
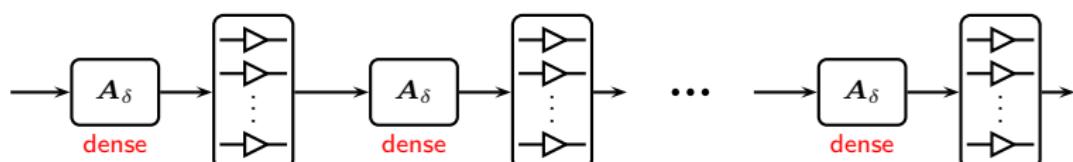
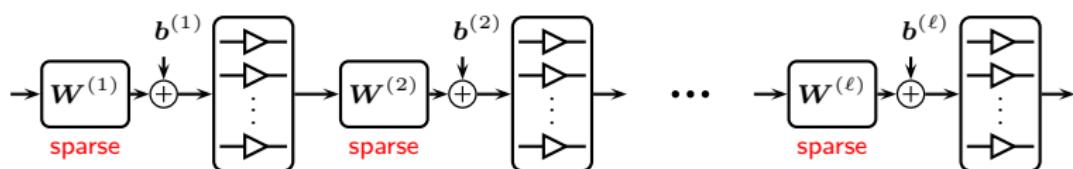


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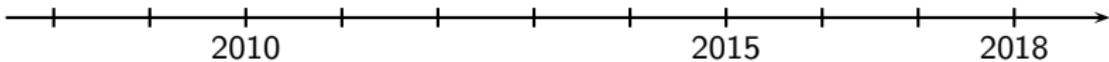


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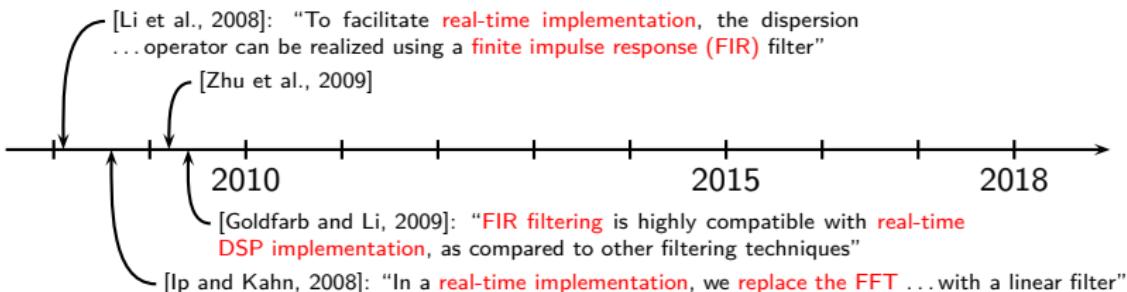


finite impulse response (FIR) filters ?

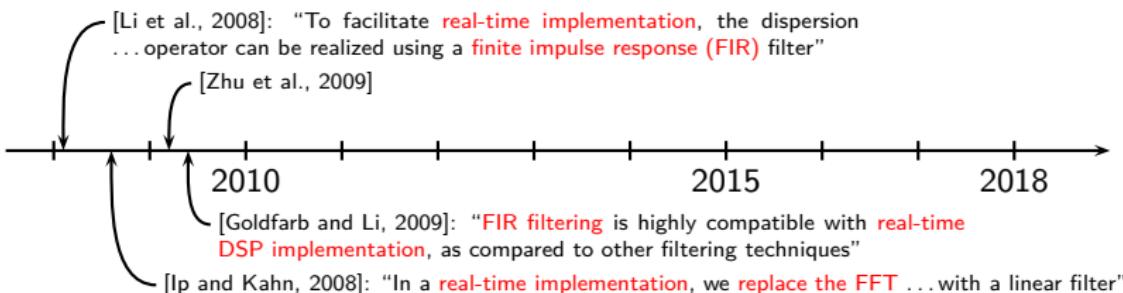
Time-Domain Digital Backpropagation: Literature



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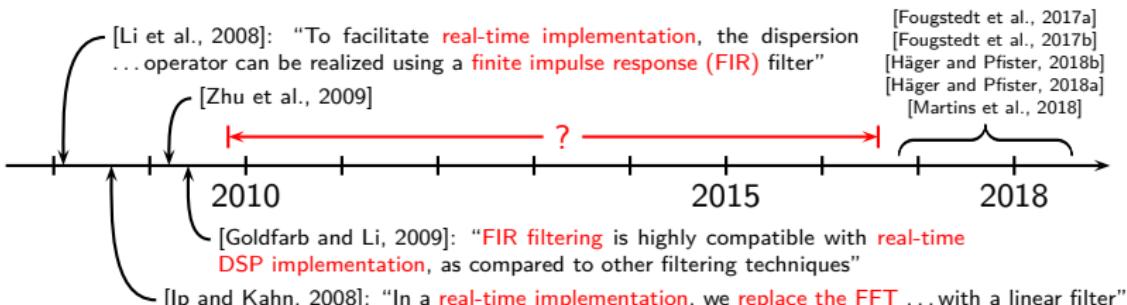


Nontrivial to achieve a good performance–complexity tradeoff!

Example for $R_{\text{symb}} = 10.7 \text{ Gbaud}$, $L = 2000 \text{ km}$ [Ip and Kahn, 2008]

> 1000 total taps required \implies 100× more operations than linear equalization

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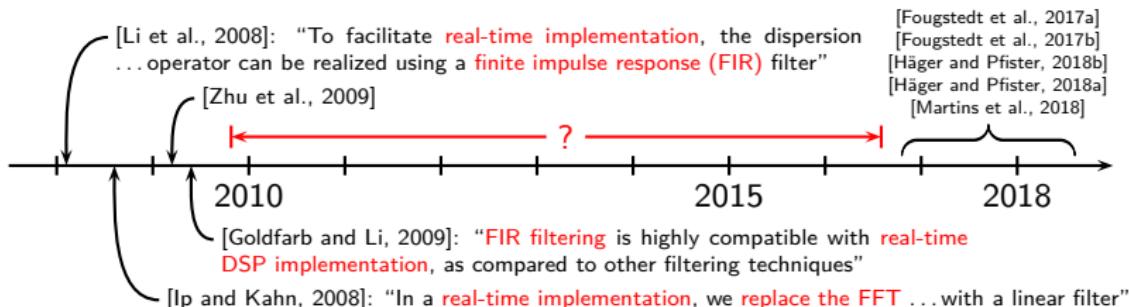


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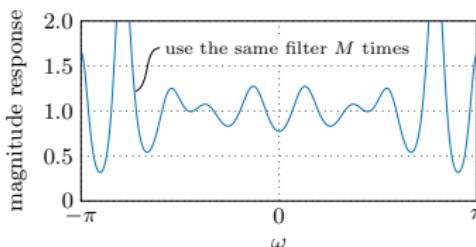
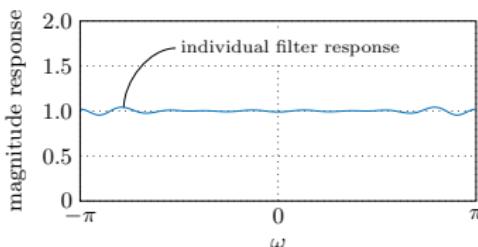
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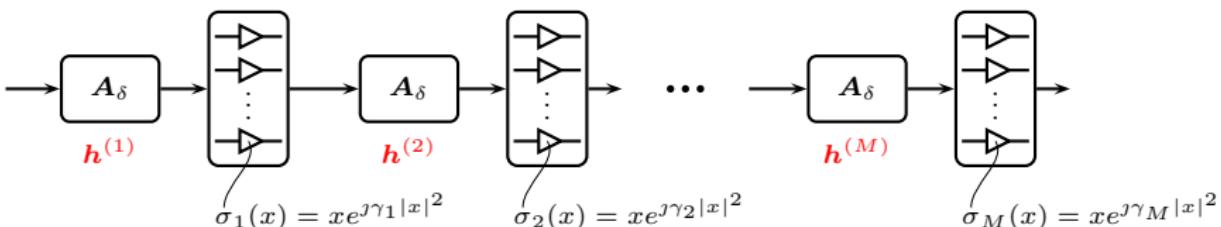
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Learned Digital Backpropagation

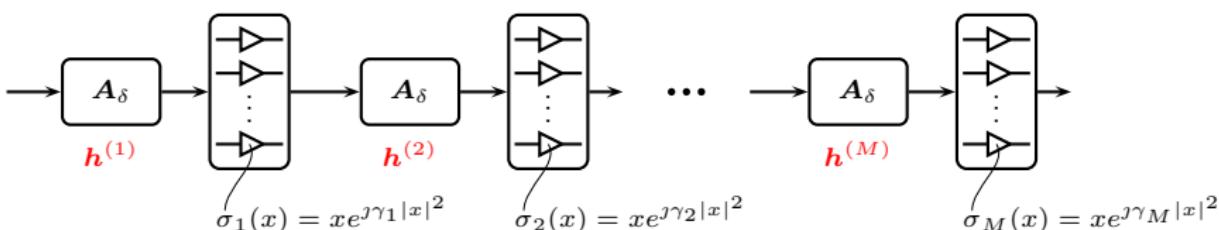
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TensorFlow implementation of the computation graph $f_\theta(y)$:



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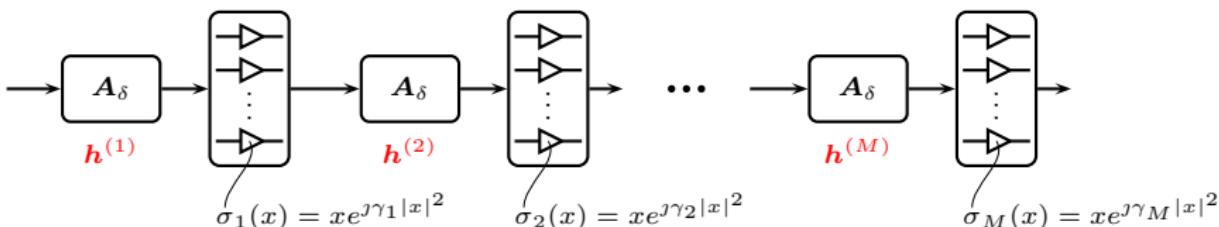
Deep learning of parameters $\theta = \{\mathbf{h}^{(1)}, \dots, \mathbf{h}^{(M)}\}$:

$$\min_{\theta} \sum_{i=1}^N \text{Loss}(f_\theta(\mathbf{y}^{(i)}), \mathbf{x}^{(i)}) \triangleq g(\theta) \quad \begin{array}{l} \text{using } \theta_{k+1} = \theta_k - \lambda \nabla_{\theta} g(\theta_k) \\ \text{Adam optimizer, fixed learning rate} \end{array}$$

mean squared error

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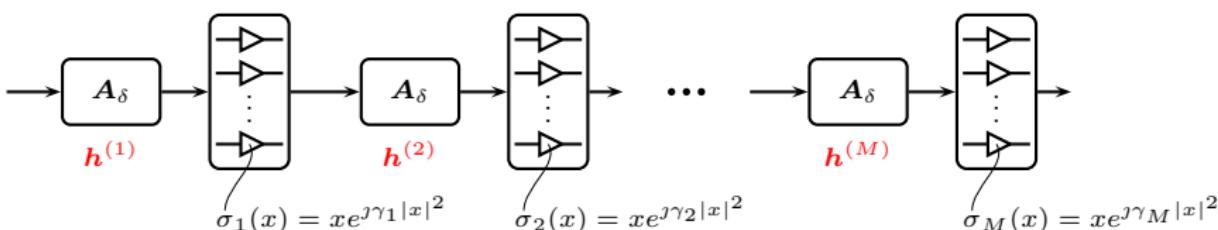
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Adam optimizer, fixed learning rate

- How to choose the starting point θ_0 and get short filters?

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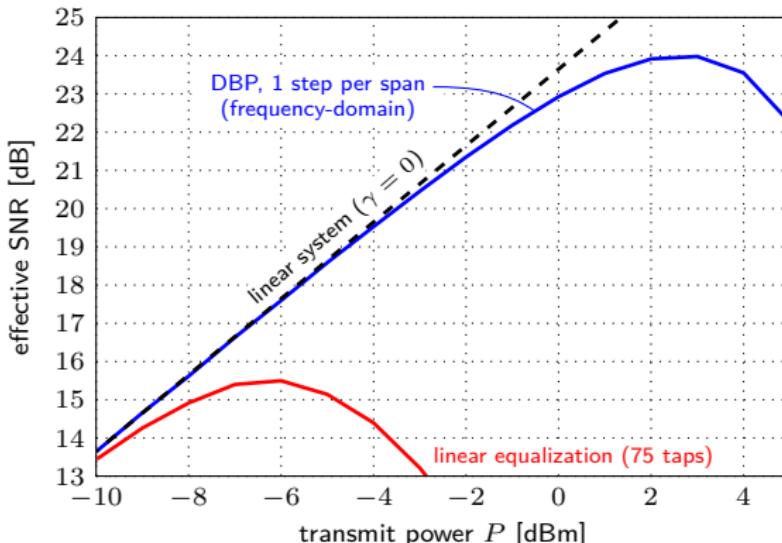
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- How to choose the starting point θ_0 and get short filters?
- Iteratively prune (set to 0) the outermost filter taps during gradient descent until a certain target filter length is reached

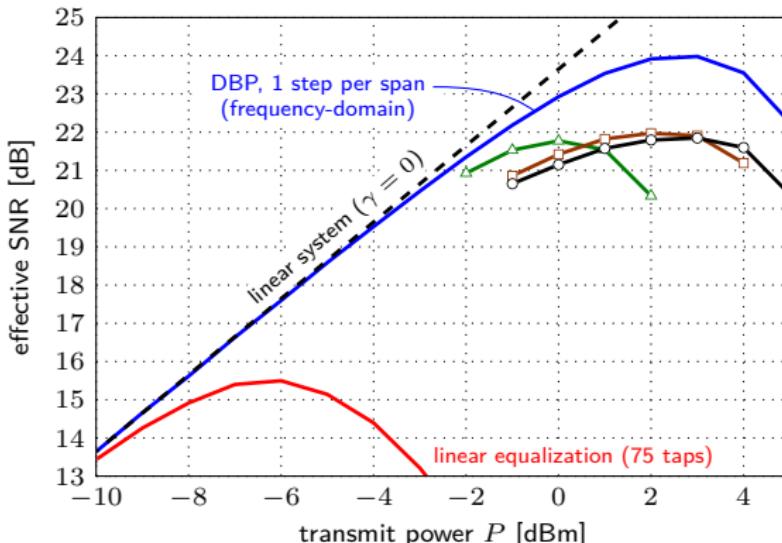
Optimization Results (10.7 Gbaud)



Parameters similar to [Ip and Kahn, 2008]:

- 25×80 km SSFM
- Gaussian modulation
- RRC pulses (0.1 roll-off)
- 10.7 Gbaud
- 2 samples/symbol processing
- single channel, single pol.

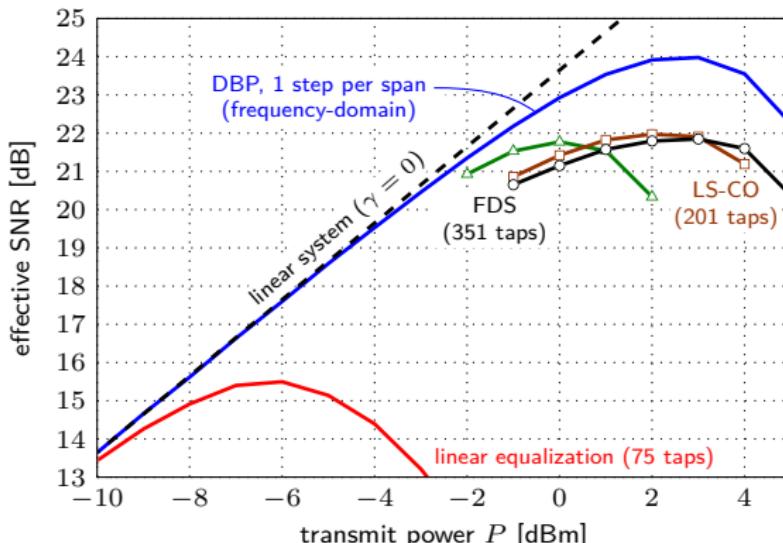
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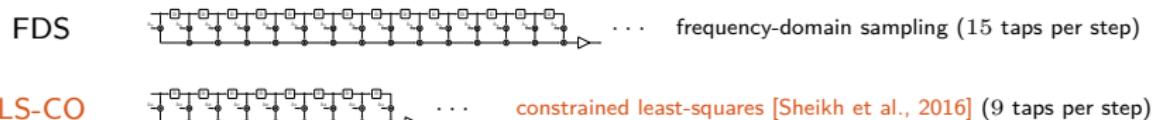
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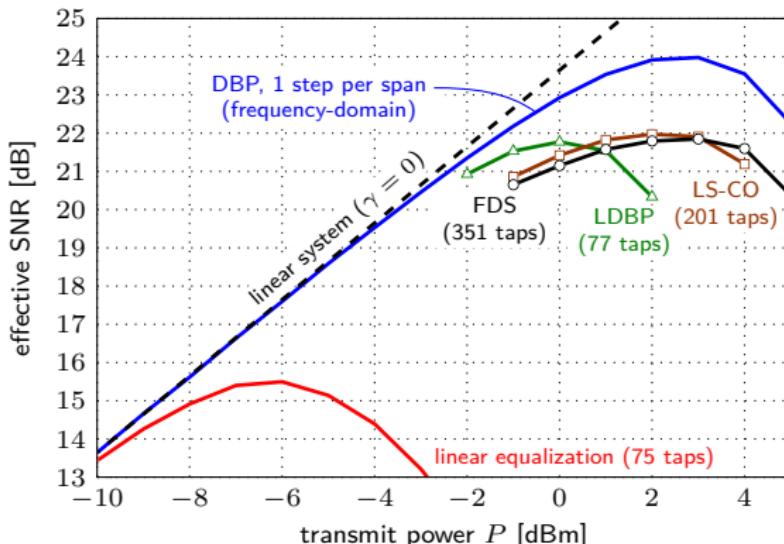


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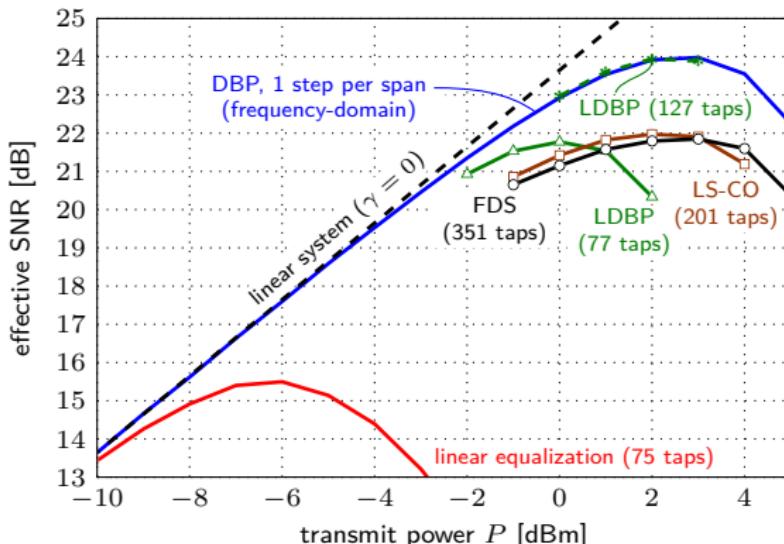


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Filter Coefficient Quantization

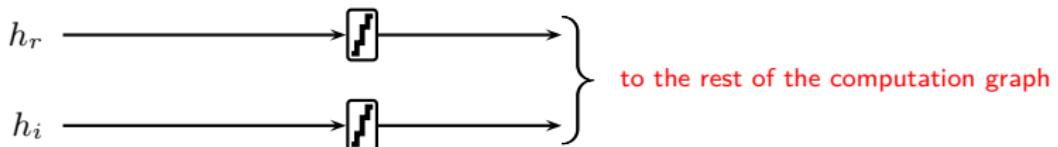
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DSP implementation requires **quantized coefficients** [Fougstedt et al., 2017a],
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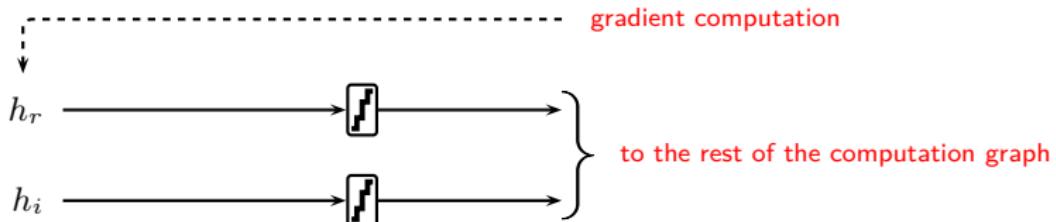
- Apply **TensorFlow's "fake quantization"** to each filter coefficient variable:



Filter Coefficient Quantization

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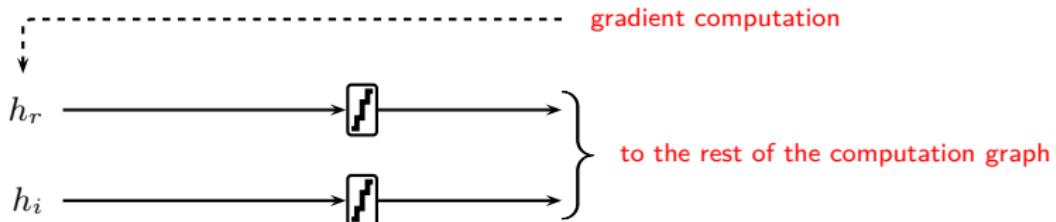


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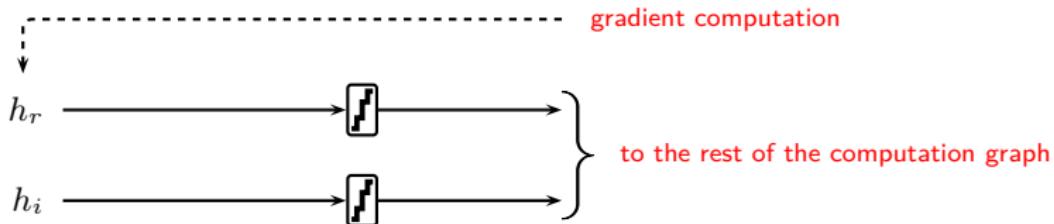


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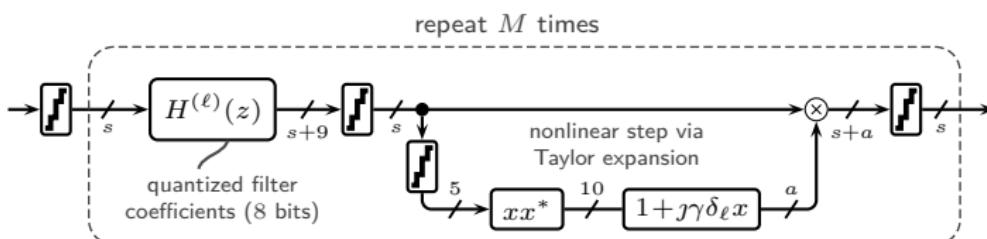
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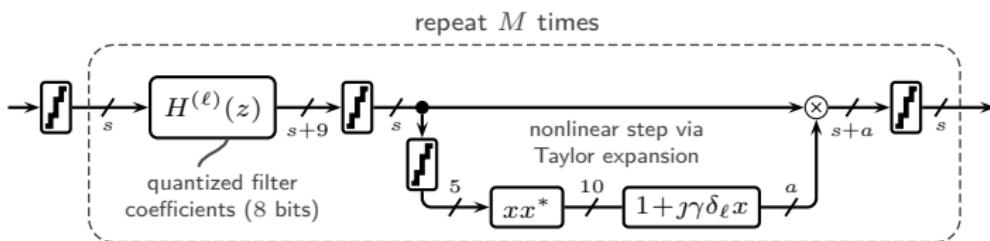


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- **Joint optimization of quantized impulse responses** \Rightarrow partial cancellation of quantization-induced frequency-response errors

Hardware Model and Circuit Implementation

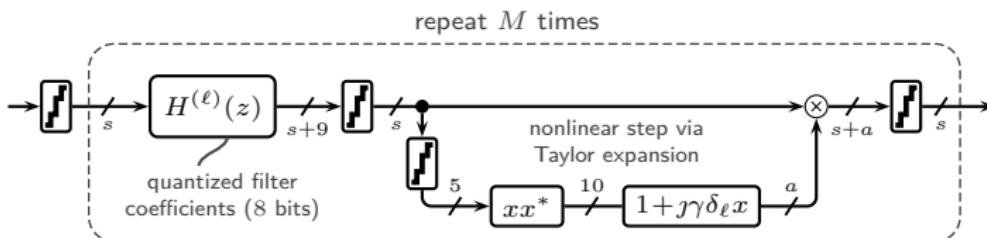


Hardware Model and Circuit Implementation



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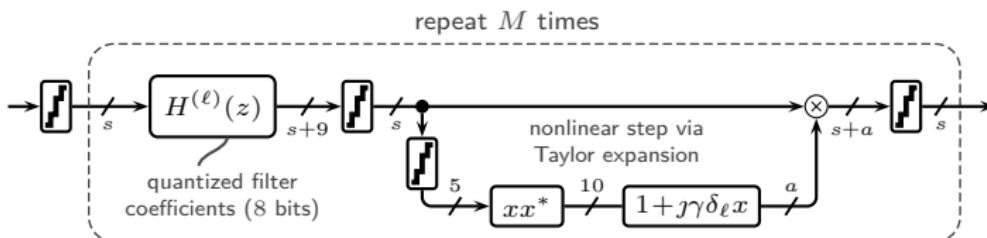
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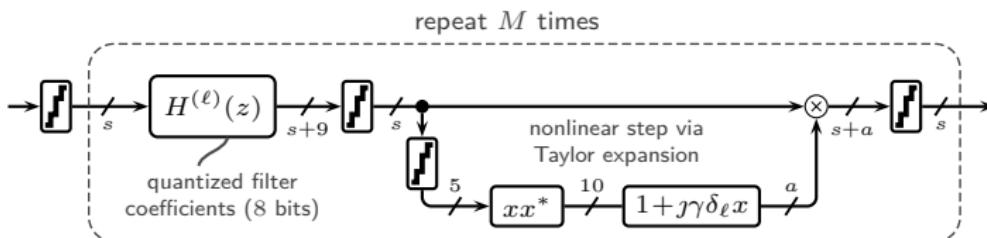


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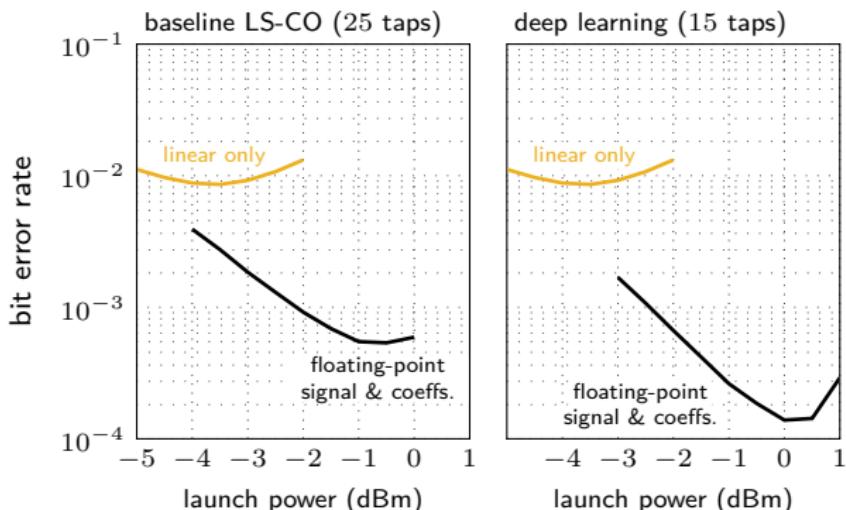


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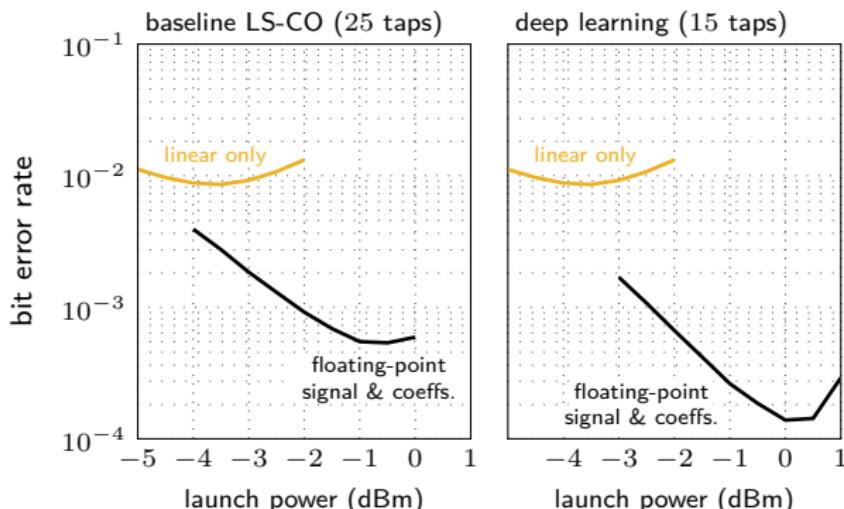
Performance and Power Consumption (20 Gbaud)



System parameters:

- $32 \times 100 \text{ km}$ fiber
- 1 step per span
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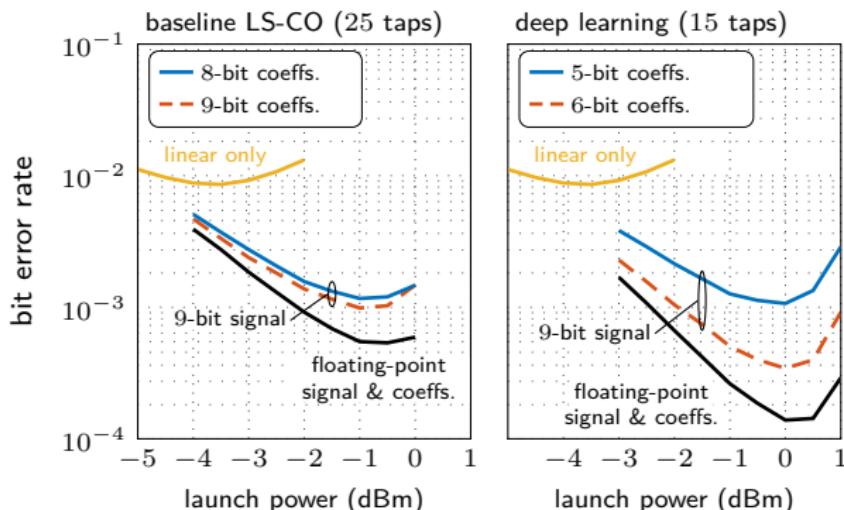


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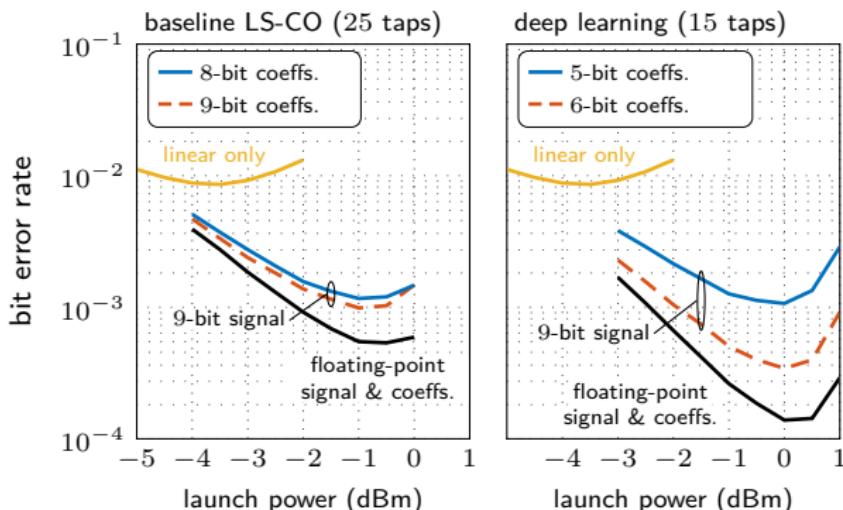


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- **8–9 signal bits required** in both cases, depending on performance
- Deep learning leads to **significantly fewer bits for the filter taps**

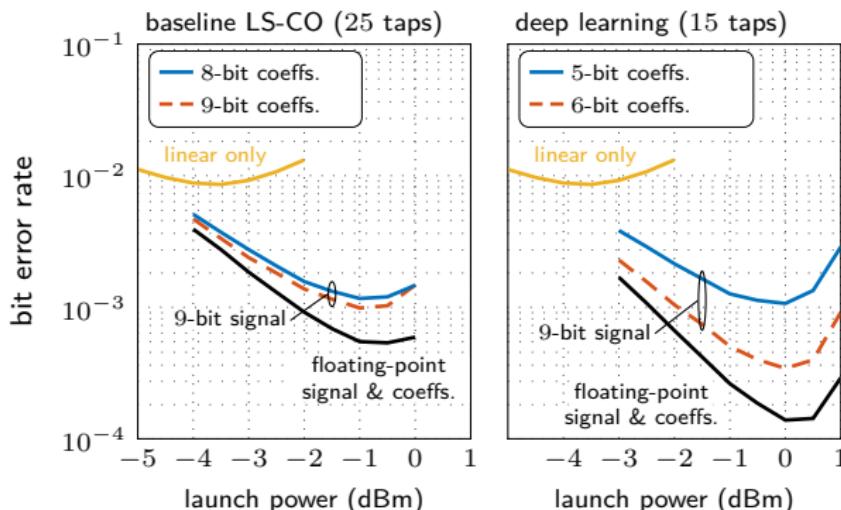
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ASIC power
per step (100 km)
0.37 W (9-bit coeffs.)

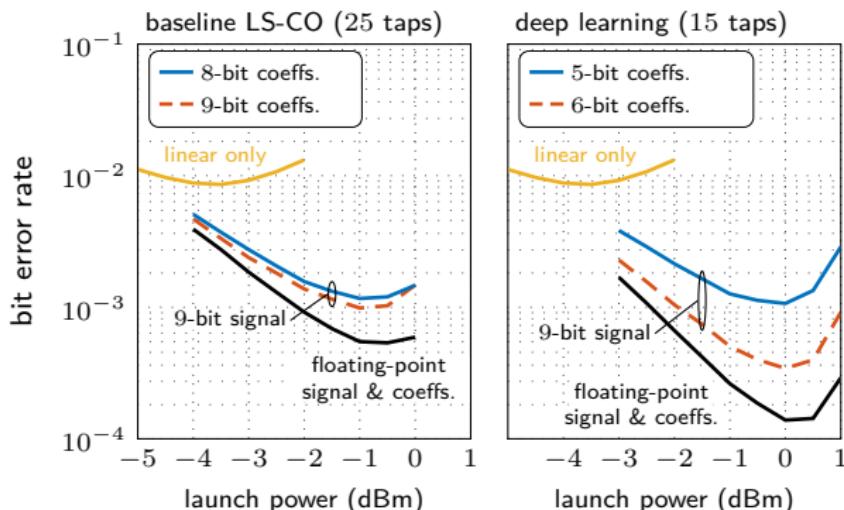
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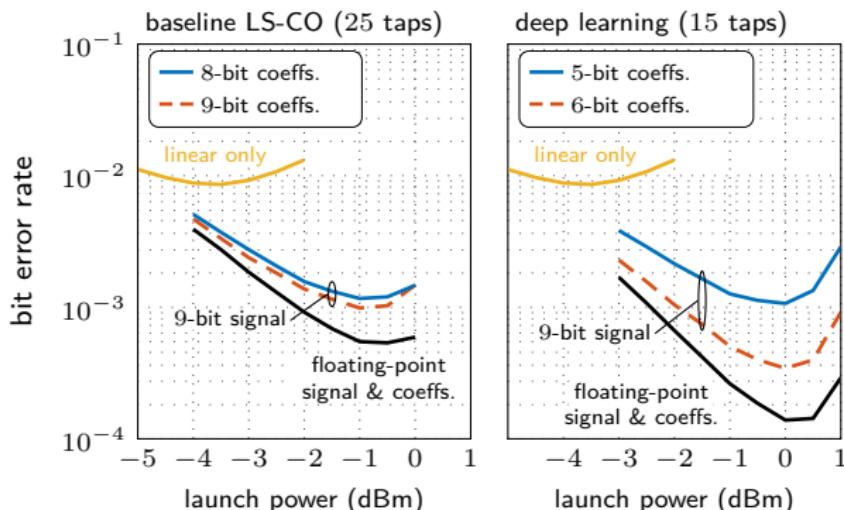
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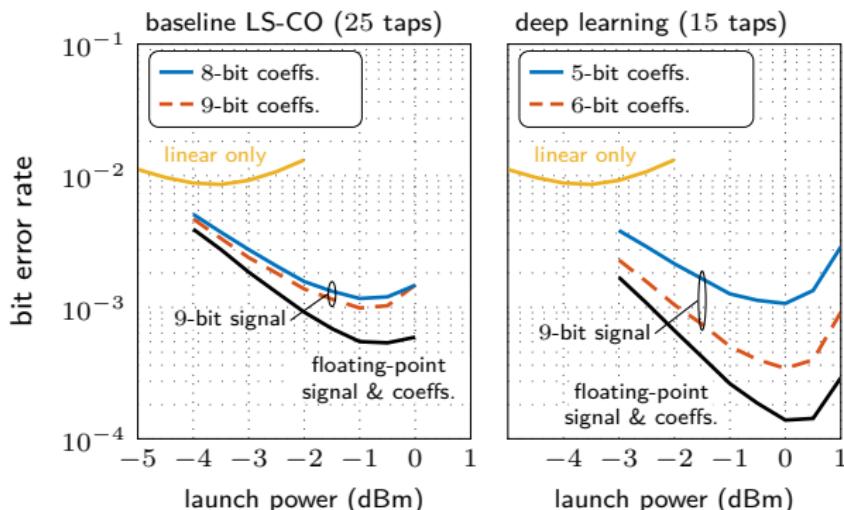
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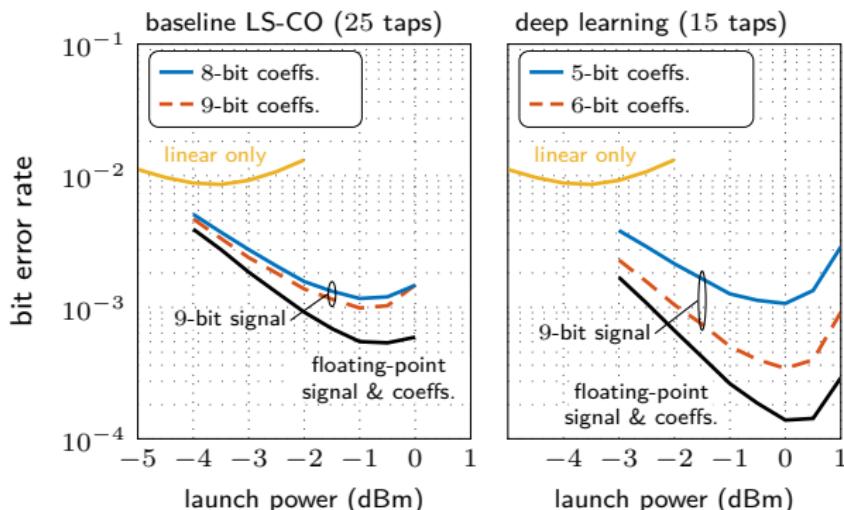
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Outline

1. Introduction
2. Connection between Deep Learning and Digital Backpropagation
3. ASIC Implementation Aspects
4. Wideband Digital Backpropagation via Subband Processing
5. Conclusions

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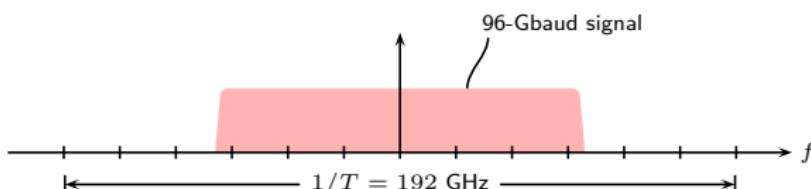
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Question

Is it possible to scale the time-domain / deep learning approach gracefully to larger bandwidths?

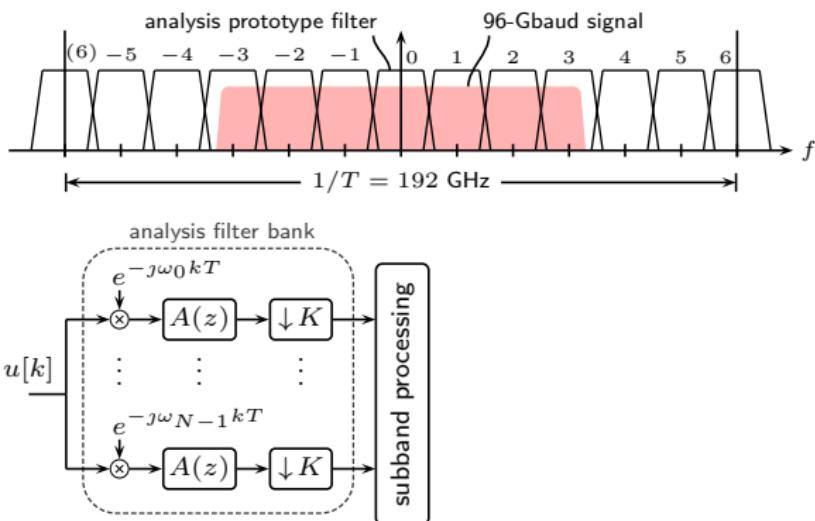
Subband Processing via Filter Banks

See, e.g., [Taylor, 2008], [Ho, 2009], [Slim et al., 2013], [Nazarathy and Tolmachev, 2014] ([linear comp.](#)) and [Mateo et al., 2010], [Ip et al., 2011], [Oyama et al., 2015] ([nonlinear comp.](#))



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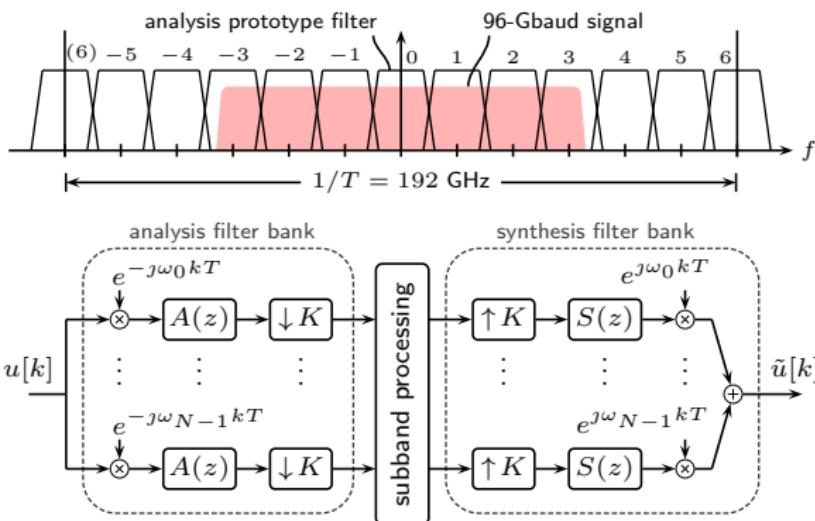
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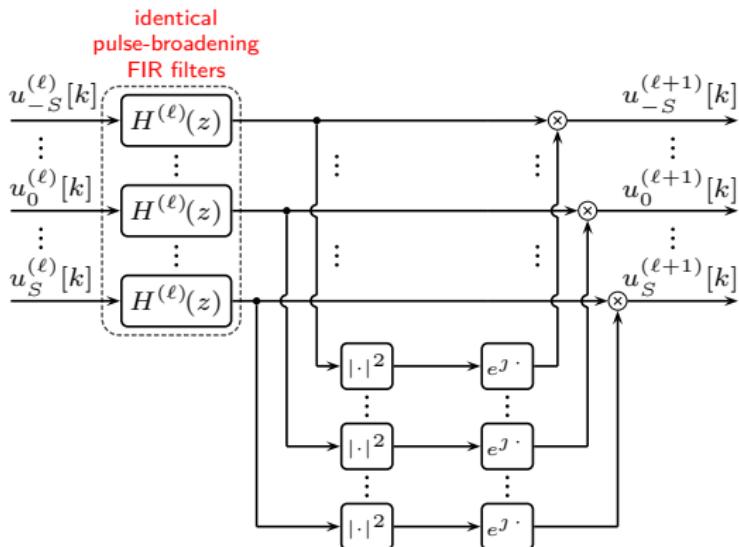
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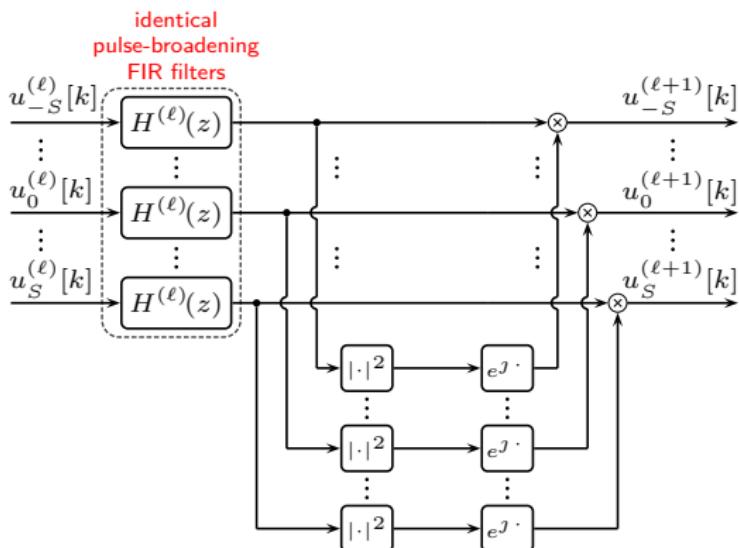


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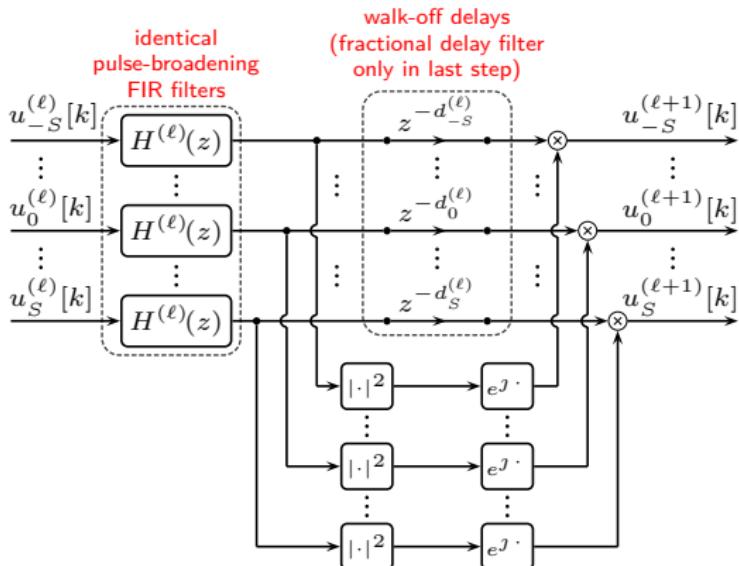


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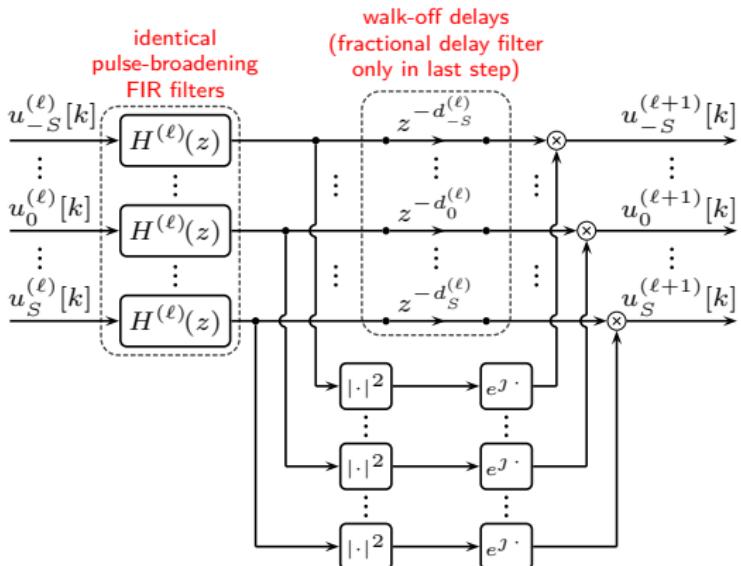
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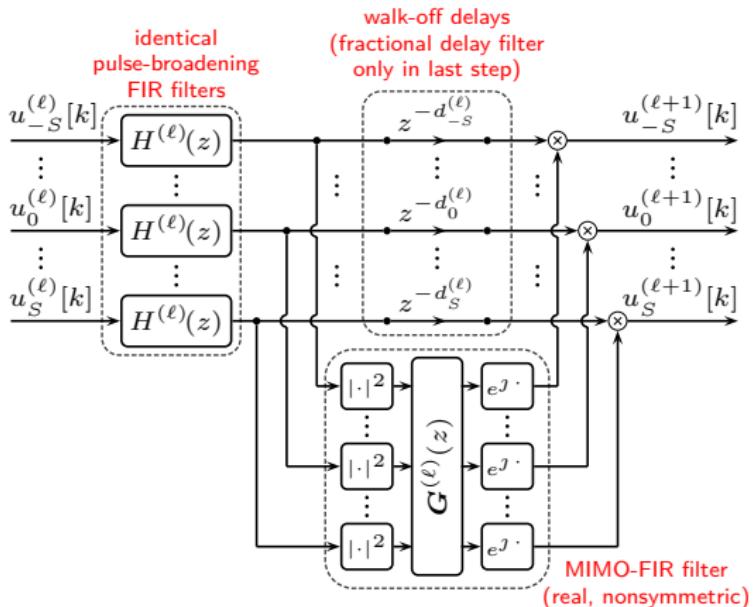


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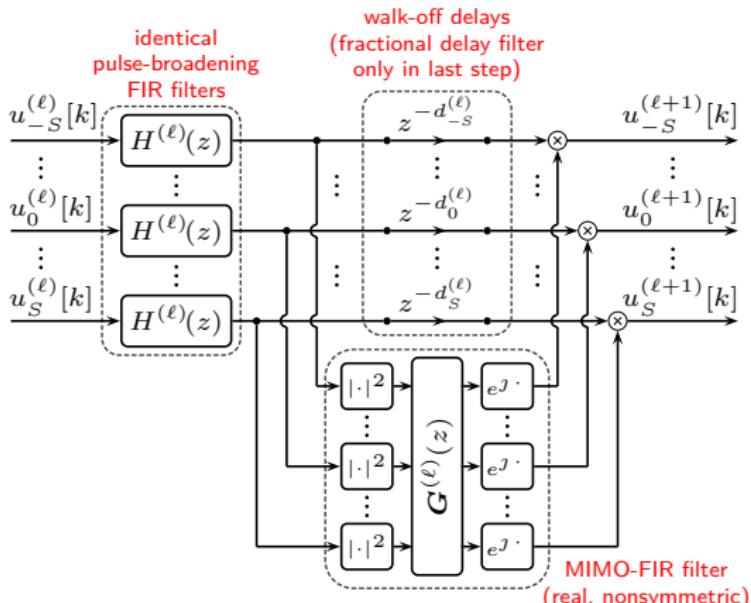
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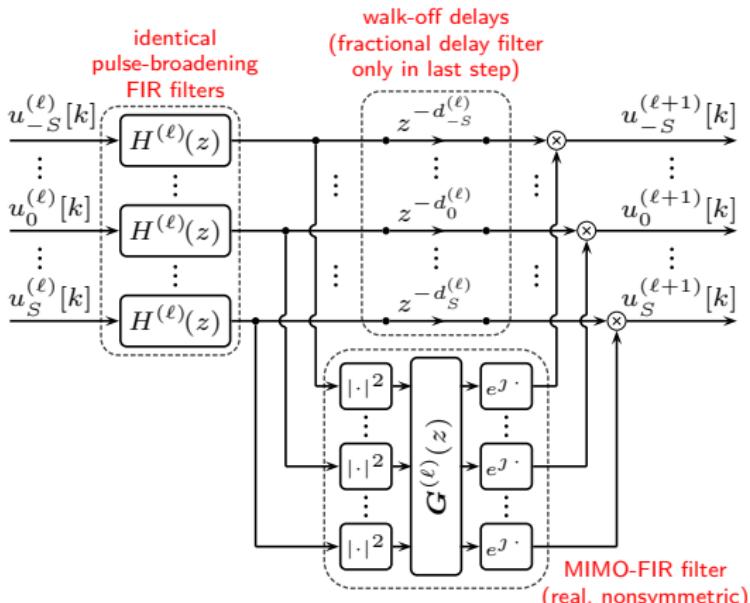


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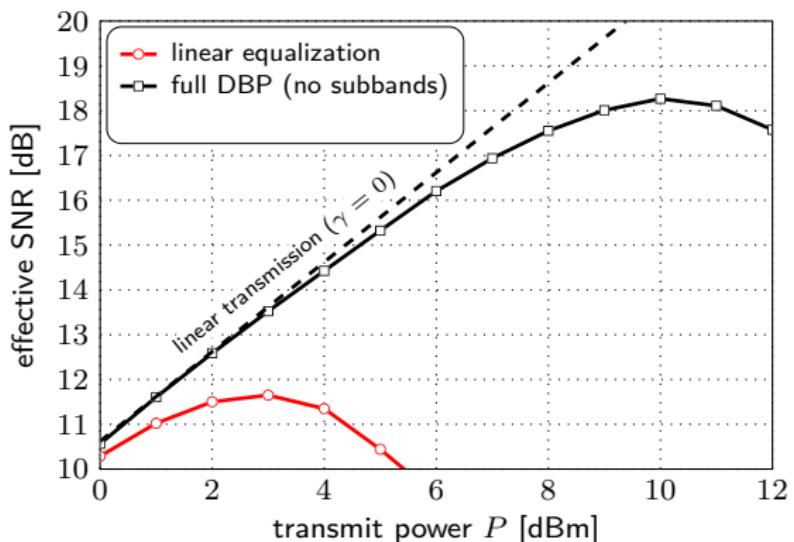
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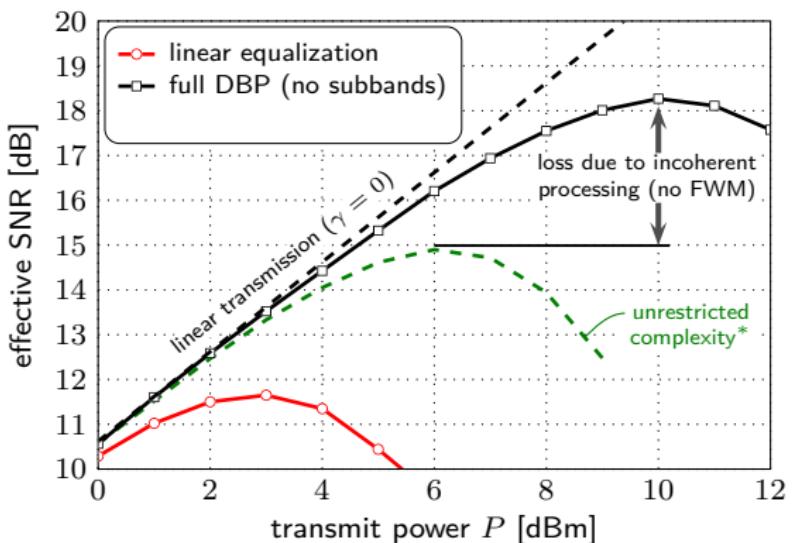
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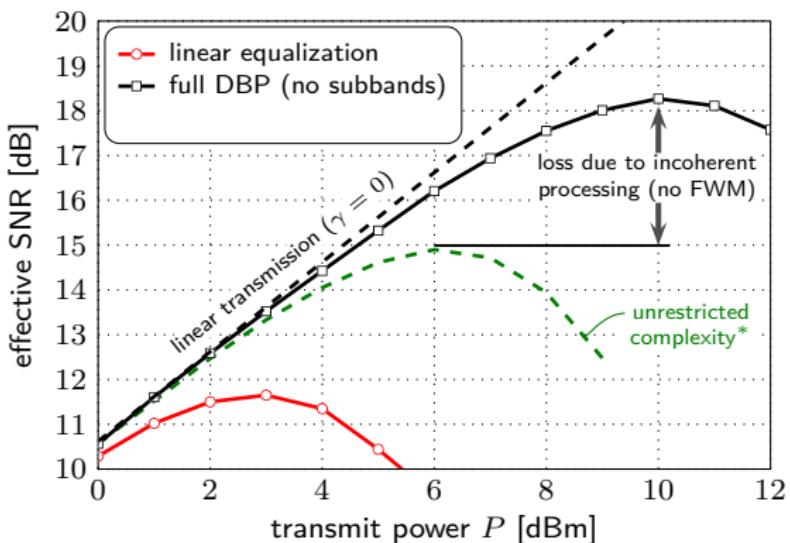
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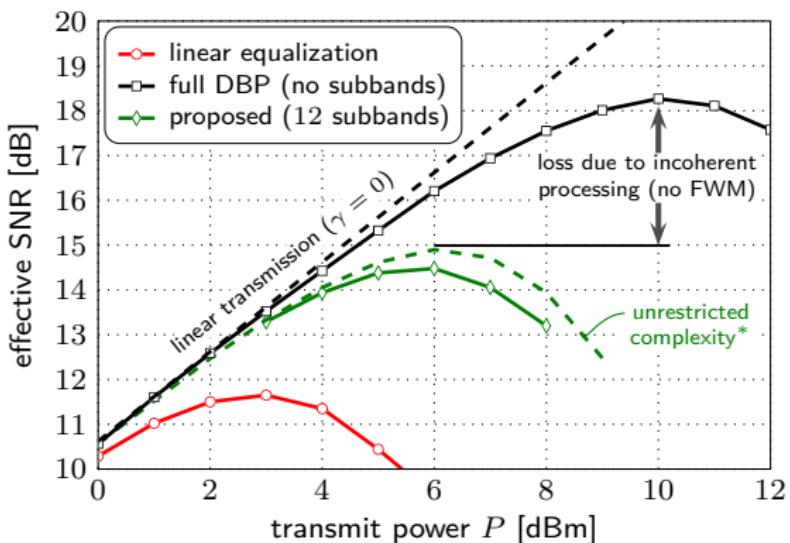
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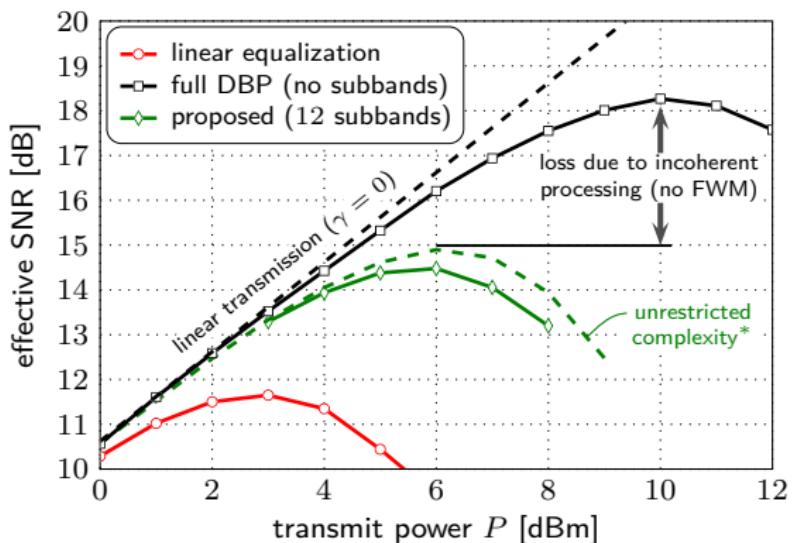
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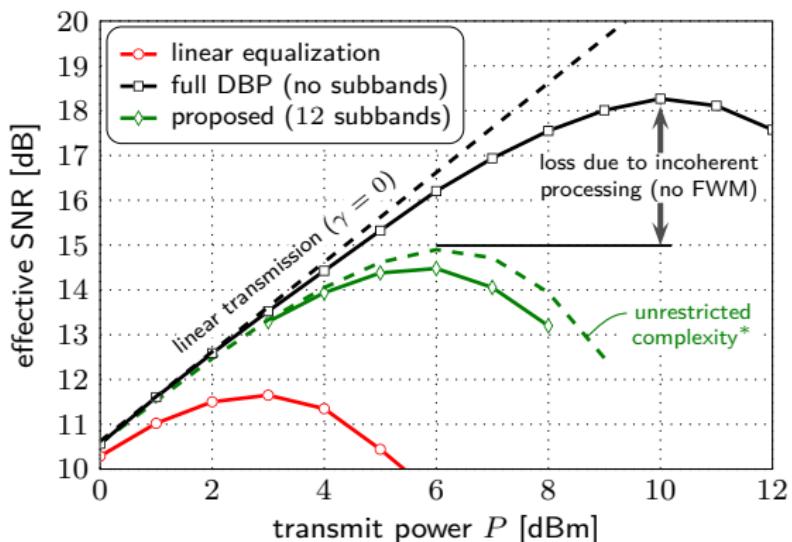
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- $\approx 2 - 3 \times$ less complexity compared to full DBP (estimated)

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- Split-step digital backpropagation appears feasible for real-time DSP implementation using a time-domain approach for the linear steps
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Thank you!



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